

ADAPTIVE COMPUTING ENGINE (ACE)

FIG. 1

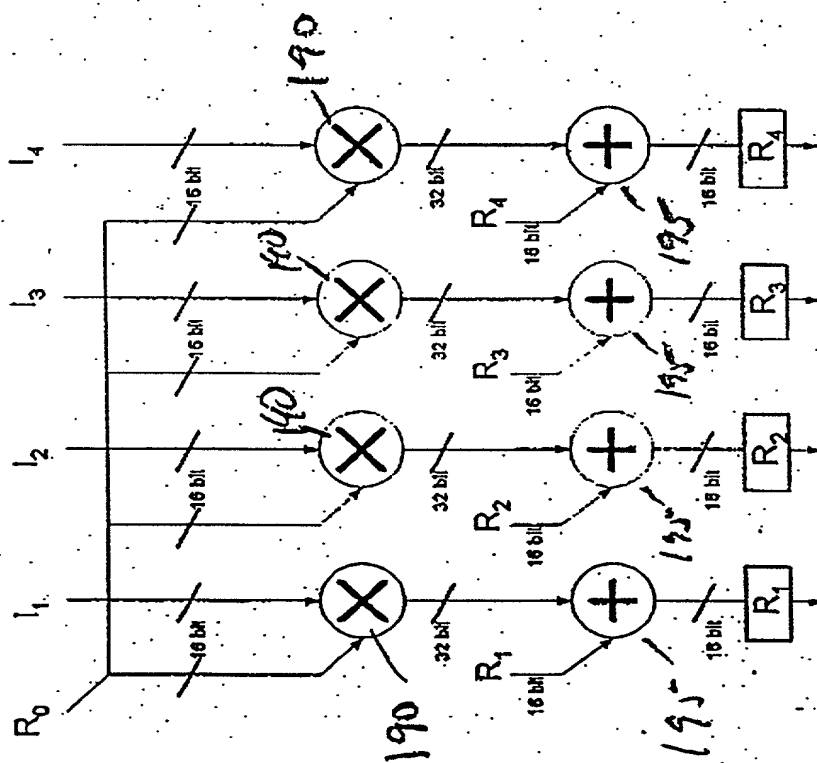


FIG. 2

TO OTHER MATRICES 150  
(INCLUDING CONTROLLER 120 AND  
MEMORY 140)

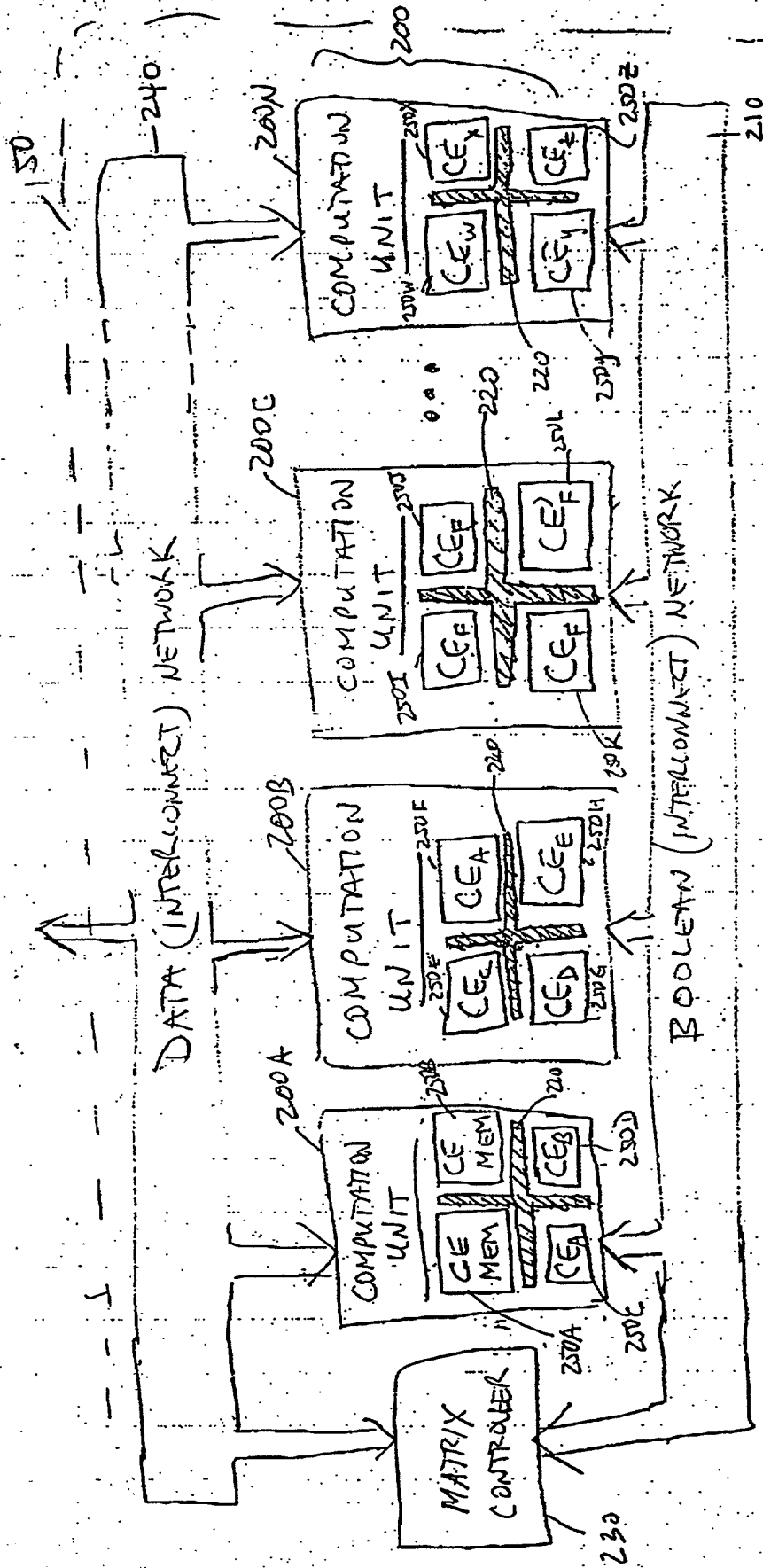
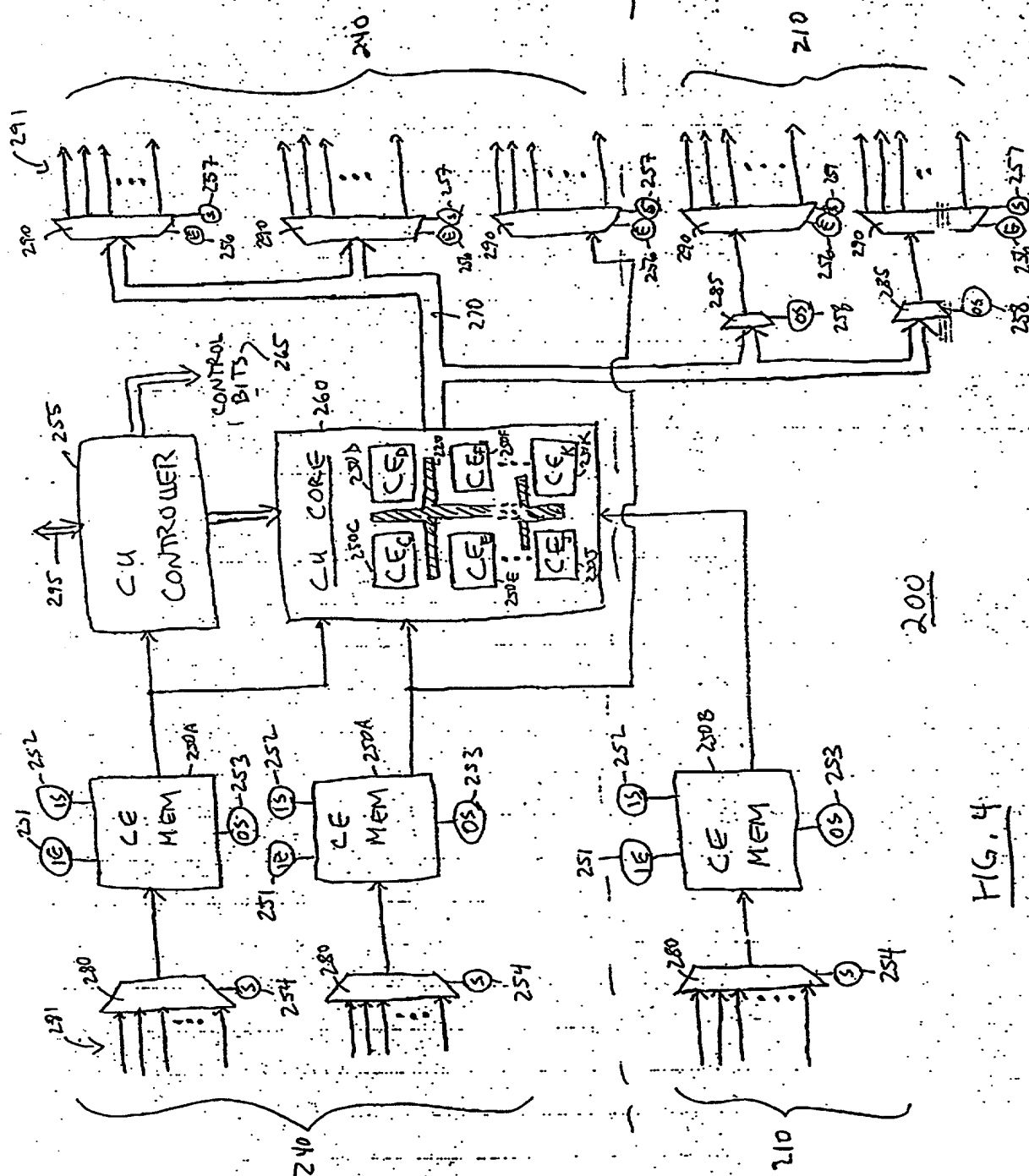


FIG. 3



415

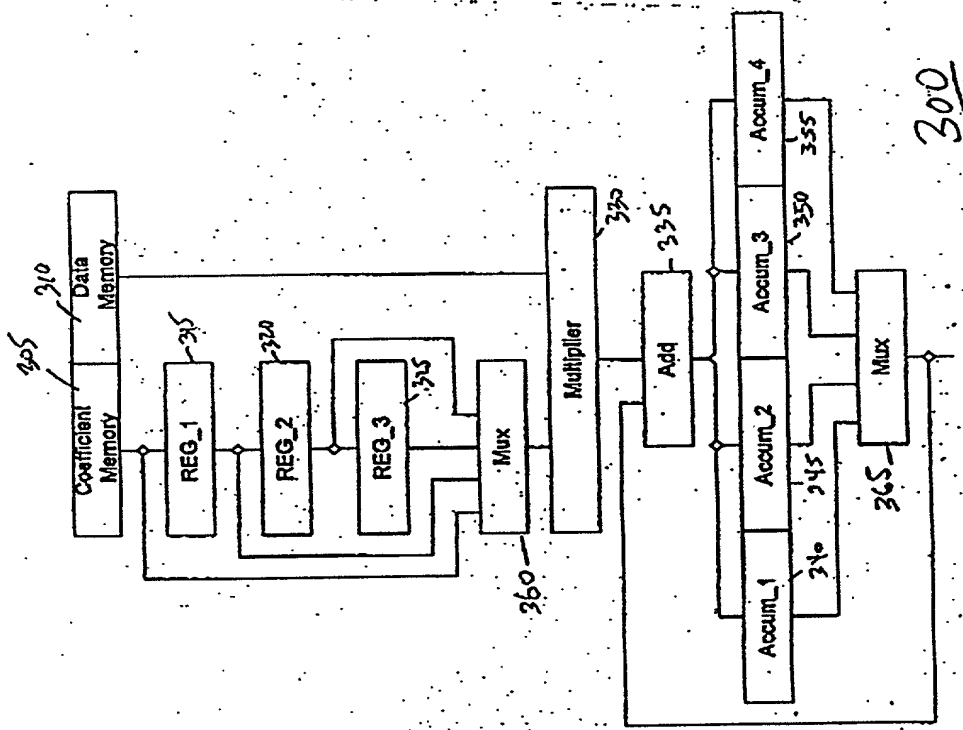


FIG. 5A

300

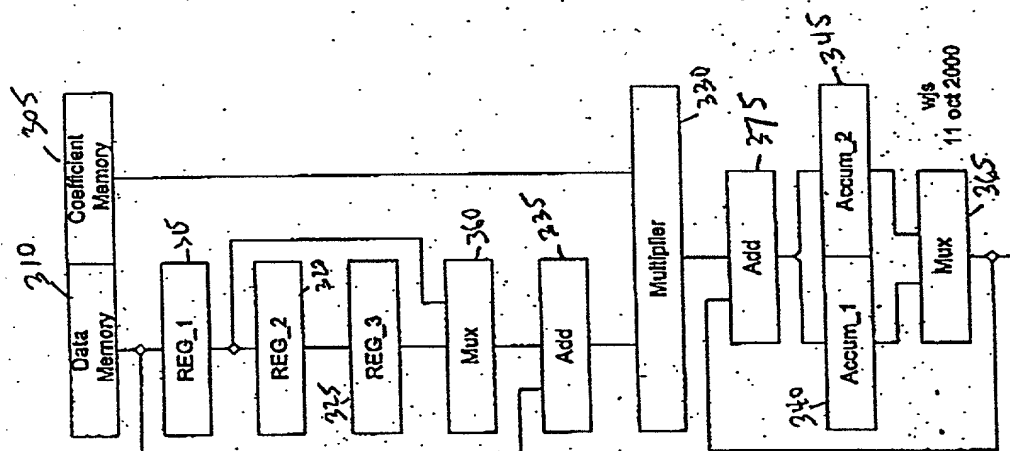
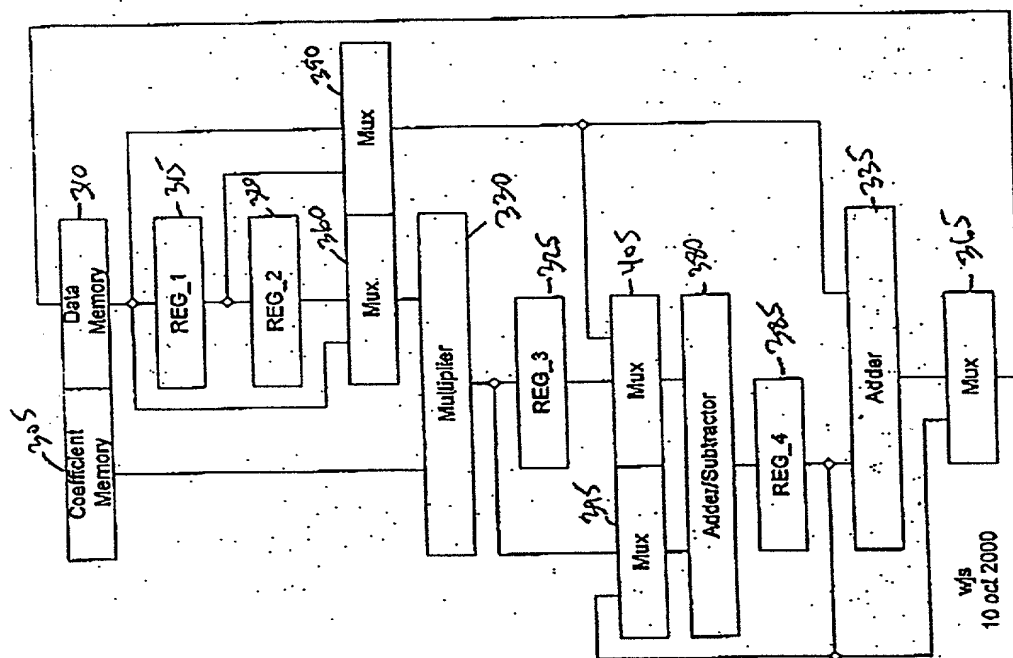


FIG. 5B



400

FIG. 5C

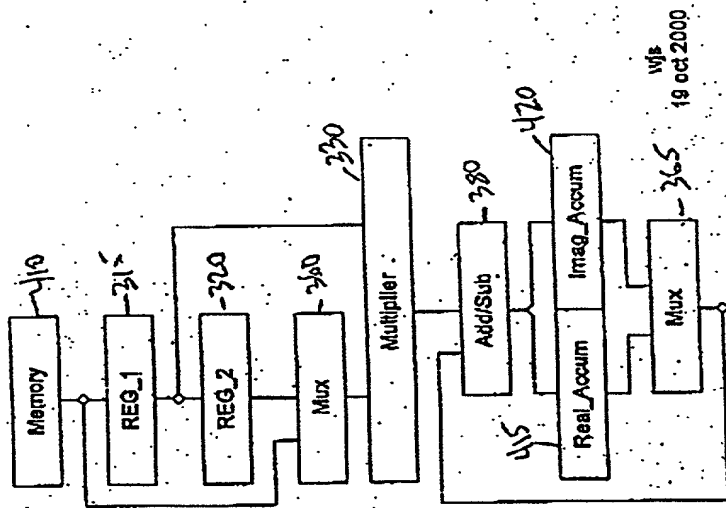
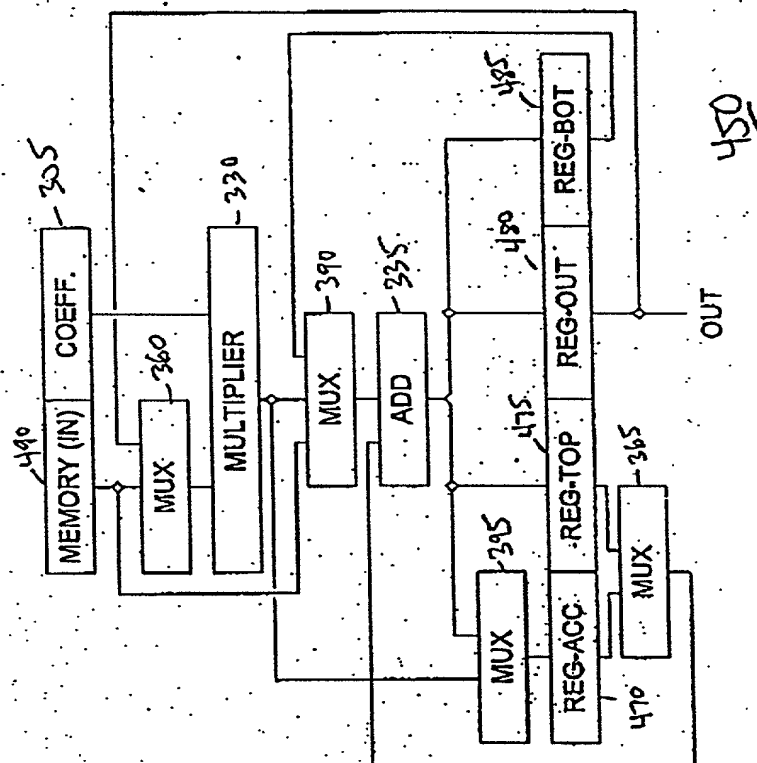
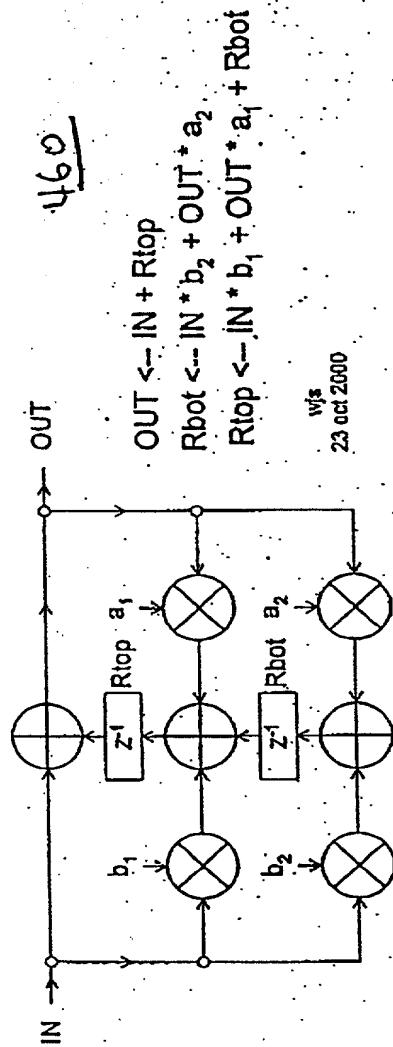


FIG. 5D

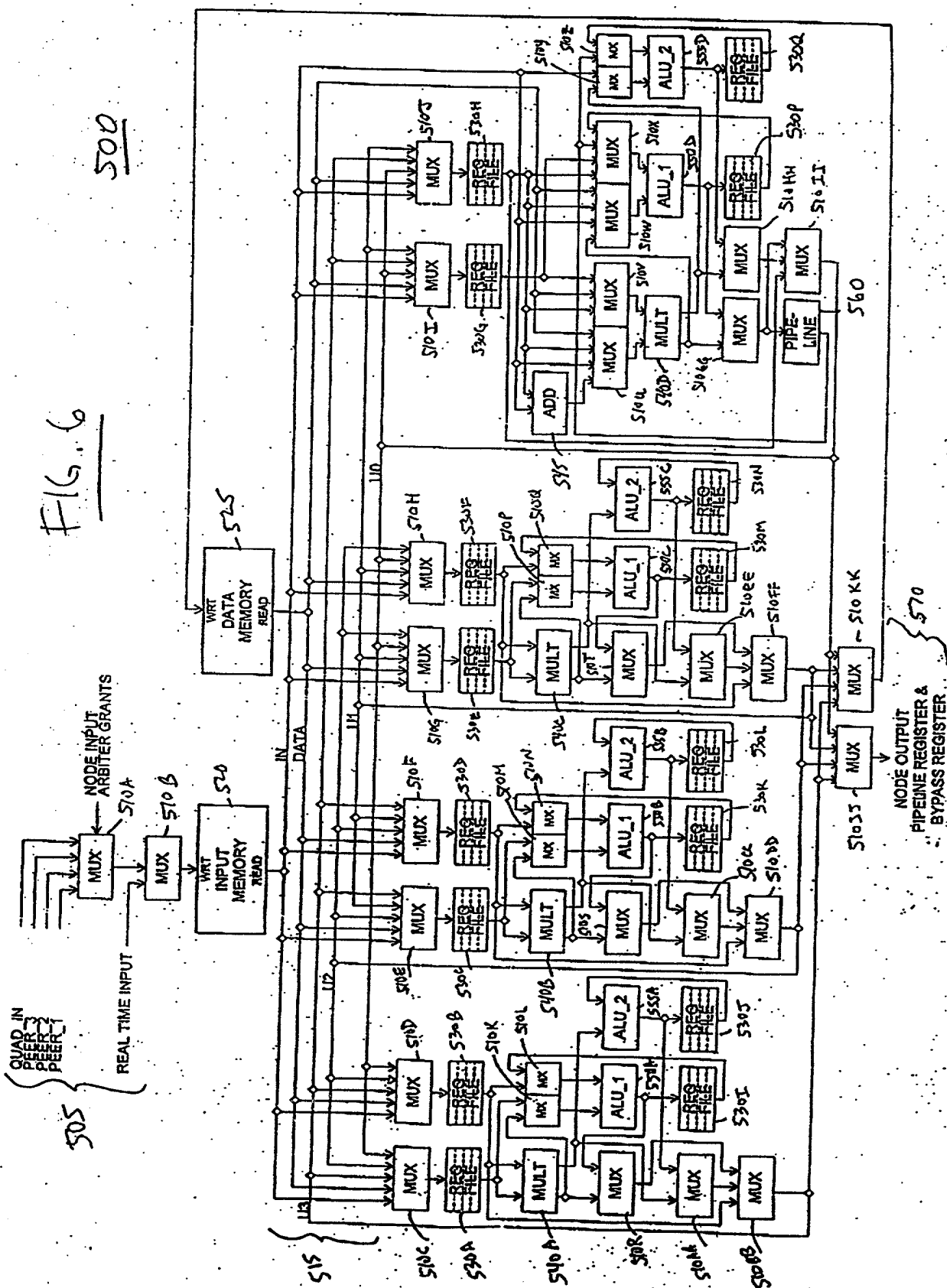


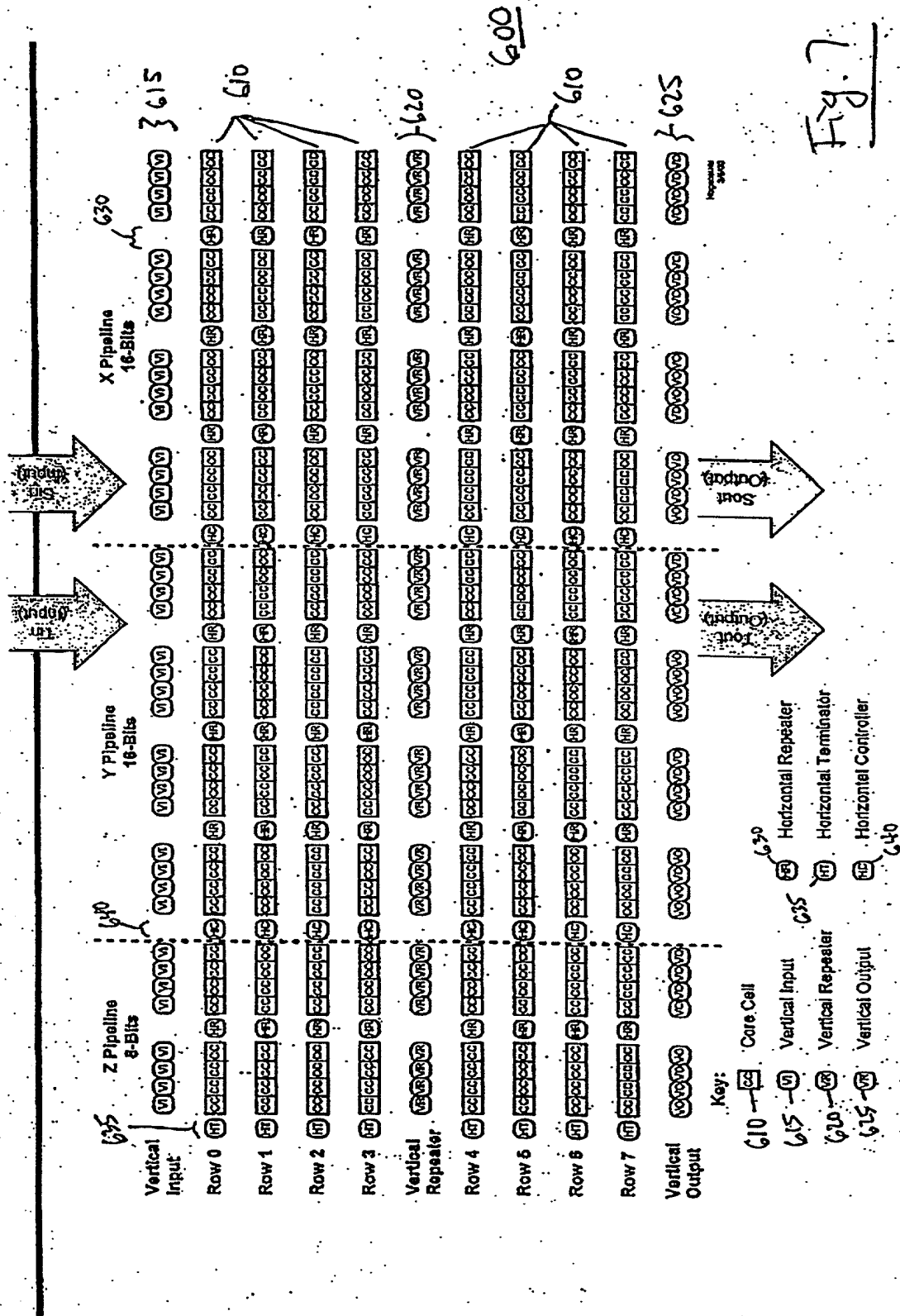


4165E

File 9

0.05





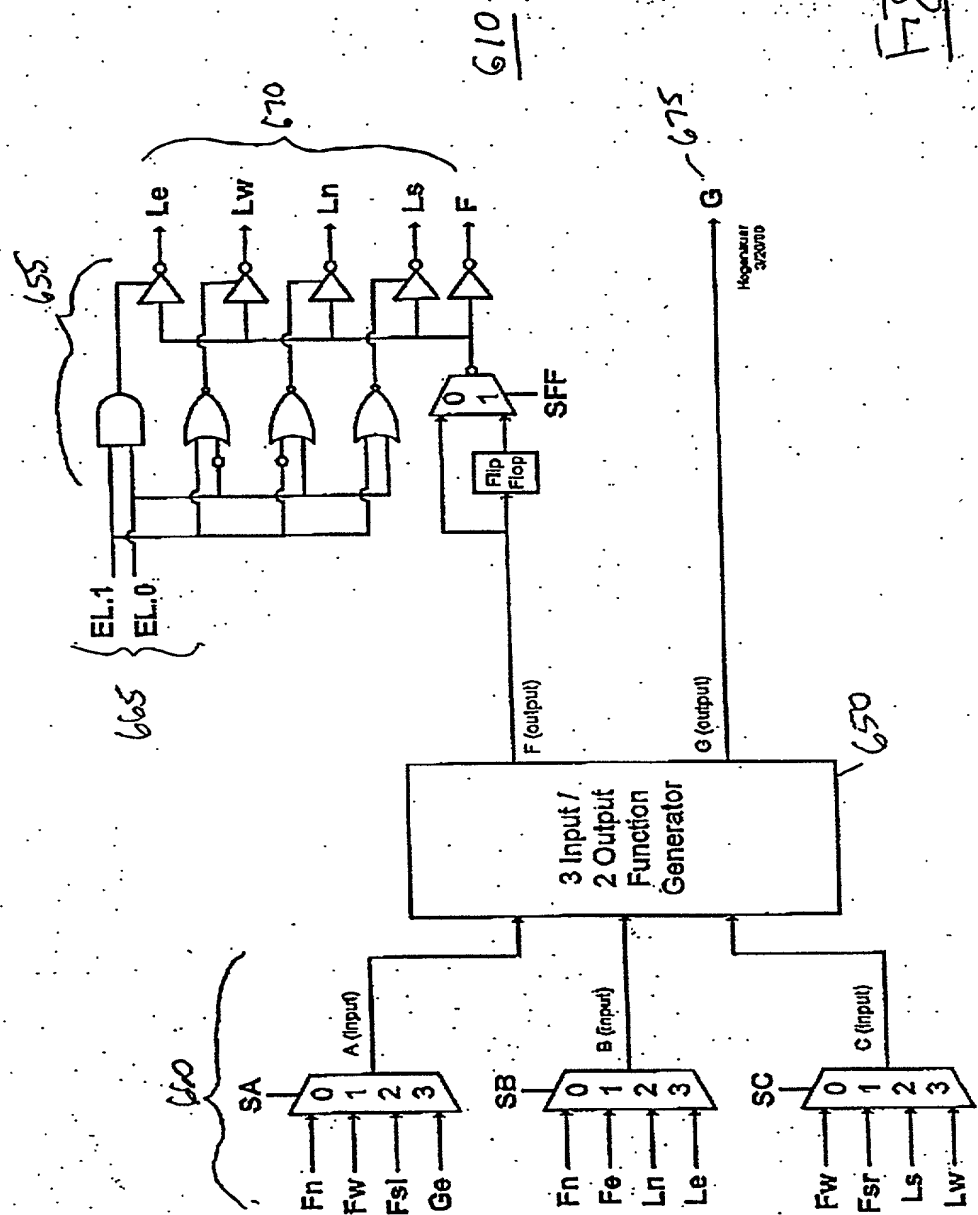


Fig. 8

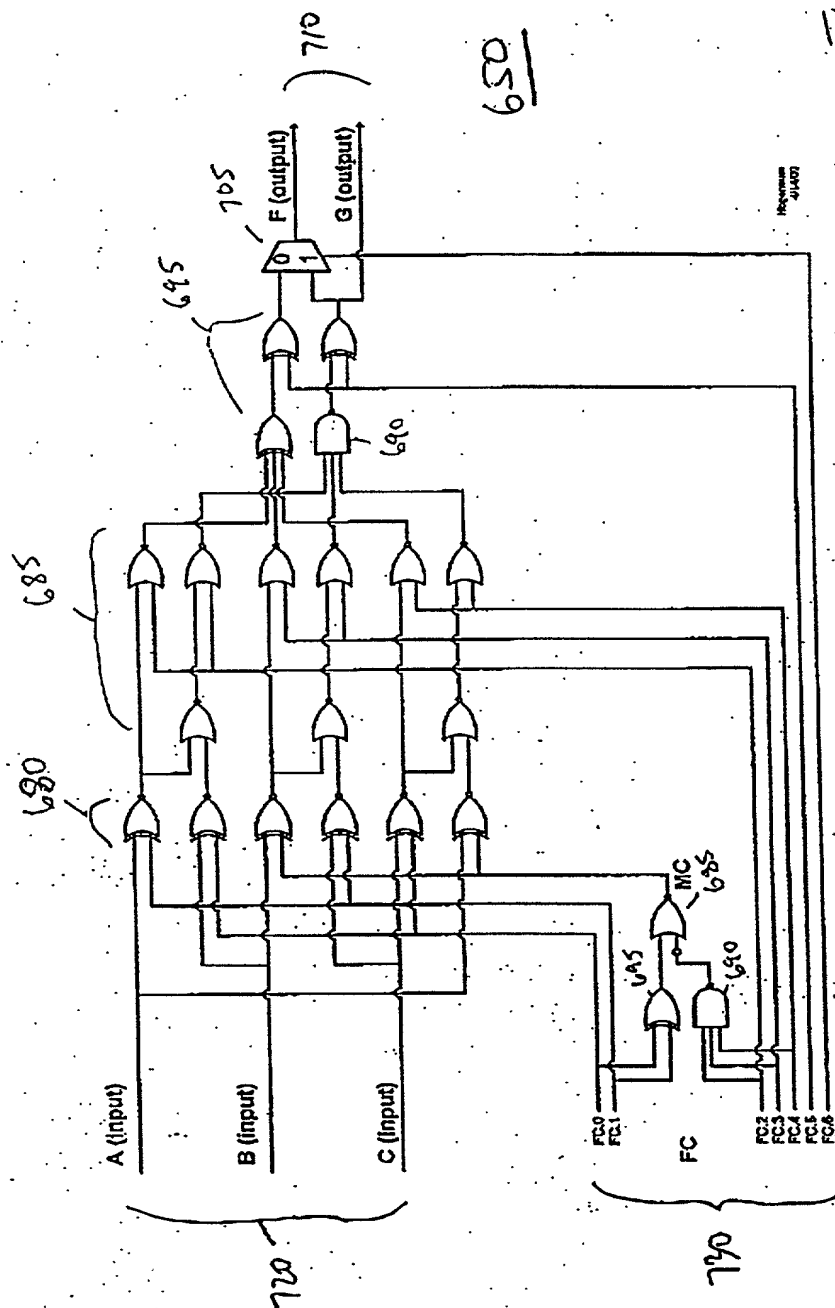
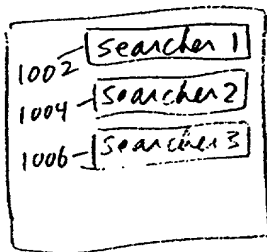


Fig. 9

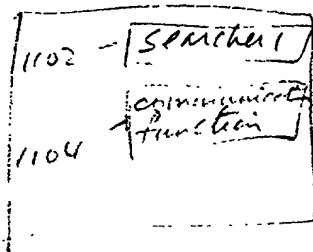


At power-up

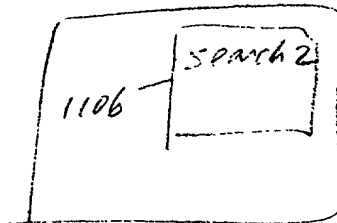


After system acquisition

FIG. 10

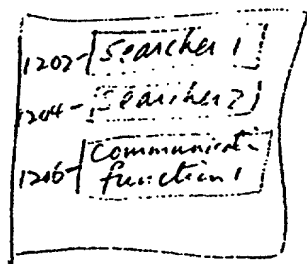


Before re-allocation

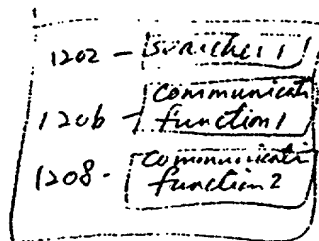


After re-allocation

FIG. 11

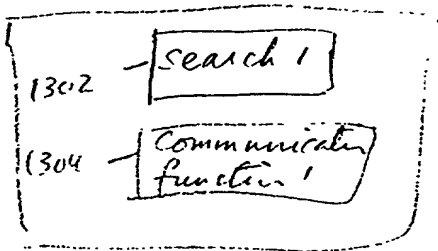


Before re-allocation

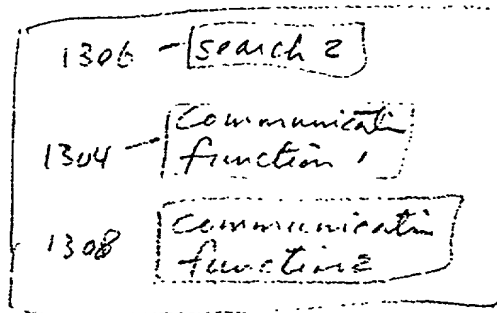


Aft. r. allocation

#16, 12



Before re-allocation



After re-allocation

FIG. 13